Device and its Drive

FIG.1A

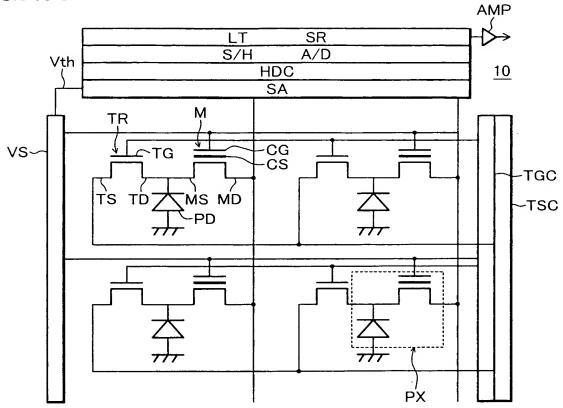
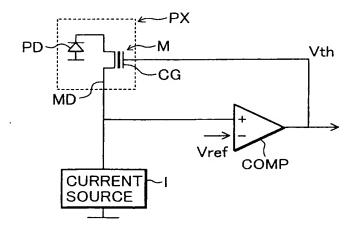


FIG.1B



Device and its Drive



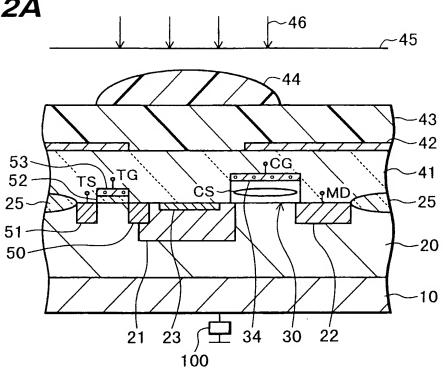


FIG.2B

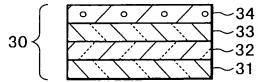


FIG.2C

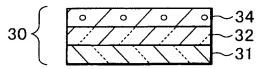
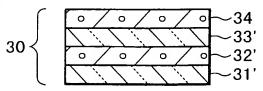


FIG.2D



and its Drive

FIG.3A

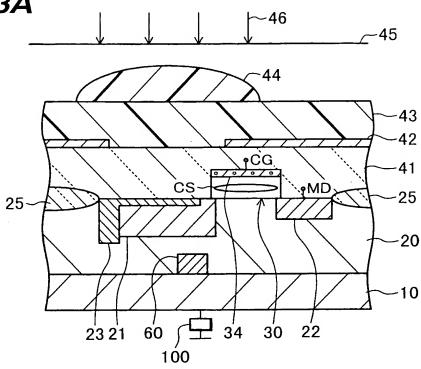
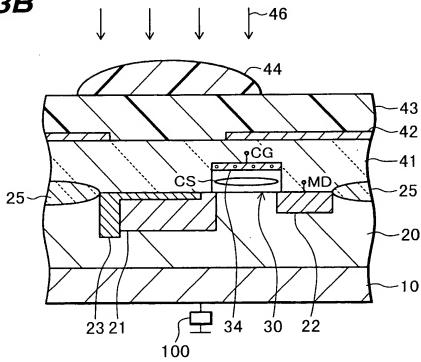


FIG.3B



and its Drive

Inventor: Makoto SHIZUKUISHI Appln. No.: New Appln. Docket No.: 107317-00061



MECHANICAL SHUTTER

p-TYPE WELL

n-TYPE SUBSTRATE

MEMORY ELEMENT DRAIN MD

MEMORY ELEMENT CONTROL GATE CG

AUXILIARY TRANSISTOR

GATE TG

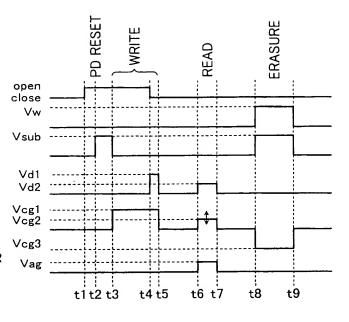


FIG.4B

MECHANICAL SHUTTER

p-TYPE WELL

n-TYPE SUBSTRATE

MEMORY ELEMENT DRAIN MD

MEMORY ELEMENT CONTROL GATE CG

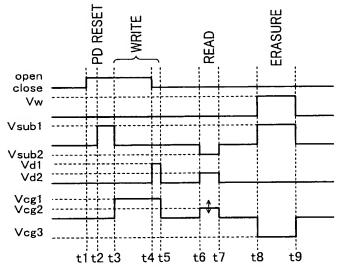


FIG.4C

p-TYPE WELL

n-TYPE SUBSTRATE

MEMORY ELEMENT DRAIN MD

MEMORY ELEMENT CONTROL GATE CG

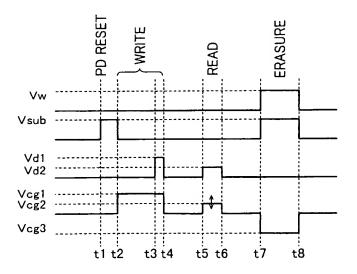
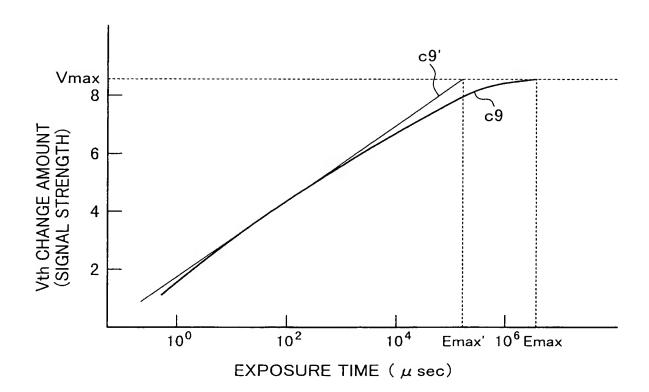
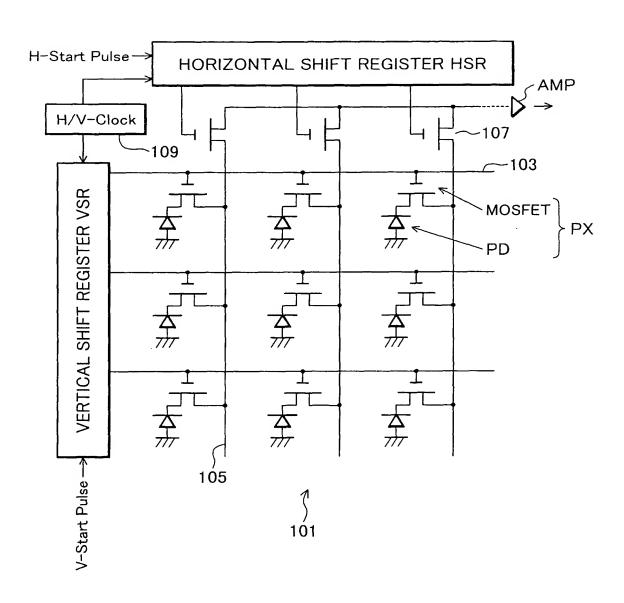


FIG.5



its Drive

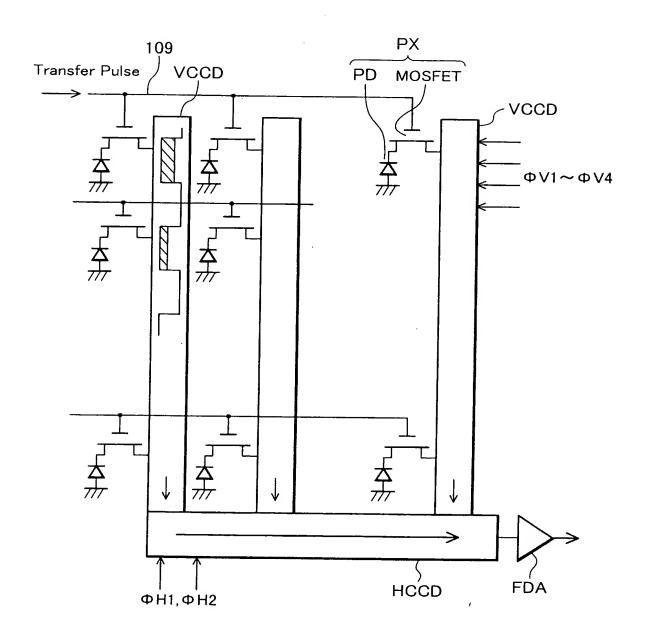
FIG.6 **PRIOR ART**



and its Drive

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FIG.7 PRIOR ART



and its Drive

FIG.8 **PRIOR ART**

